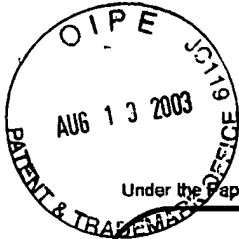


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PTO/SB/36 (08-03)

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Application Number	10/612,849
Filing Date	July 3, 2003
First Named Inventor	Mario Au
Title	FIFO Memory Devices Having Multi-Port Cache Memory Arrays Therein that Support Hidden EDC Latency and Bus Matching...
Atty Docket Number	5646-42DVIP
Group Art Unit	To Be Assigned
Examiner	To Be Assigned

A request that the above-identified application not be published under 35 U.S.C. 122(b) (nonpublication request) was included with the above-identified application on filing pursuant to 35 U.S.C. 122(b)(2)(B)(i). I hereby rescind the previous nonpublication request.

If a notice of foreign or international filing is or will be required by 35 U.S.C. 122(b)(2)(B)(iii) and 37 CFR 1.213(c), I hereby provide such notice. This notice is being provided no later than forty-five (45) days after the date of such foreign or international filing.

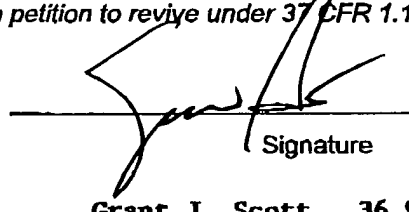
If a notice of subsequent foreign or international filing required by 35 U.S.C. 122(b)(2)(B)(iii) and 37 CFR 1.213(c) was not filed within forty-five (45) days after the date of filing of the foreign or international application, the application is ABANDONED, and a petition to revive under 37 CFR 1.137(b) is required. See 37 CFR 1.137(f).

8/13/03

Date

919/854-1400

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MYERS BIGEL SIBLEY & SAJOVEC  
Patent Attorneys  
4140 Parklake Ave., Suite 600, Raleigh, NC 27612  
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